

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Burstein et al.
Serial No. : 10/616,487
Filed : July 8, 2003
Title : DIGITAL VOLTAGE REGULATOR USING CURRENT CONTROL

Art Unit : Unknown
Examiner : Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

Under 35 USC §120, this application relies on the earlier filing date of application serial number 09/858,255, filed on May 15, 2001, now U.S. Patent No. 6,590,369. The following references were submitted to and/or cited by the Office in the prior application and, therefore, are not provided in this application:


Desig. IDs:

AA-AQQ

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 11/04/03


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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 5, 2003

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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 09464-006003	Application No. 10/616,487
	Applicant Burstein et al.		
	Filing Date July 8, 2003	Group Art Unit Unassigned	

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	3,383,584	05/14/68	Atherton			
	AB	3,978,393	08/31/76	Wisner et al.			
	AC	4,034,232	07/05/77	La Venture			
	AD	4,357,572	11/1982	Andersen et al.			
	AE	4,546,421	10/08/85	Bello et al.			
	AF	4,580,089	04/01/86	Grunsh et al.			
	AG	4,716,267	12/29/87	Reynolds			
	AH	4,761,725	08/02/88	Henze			
	AI	4,920,246	04/24/90	Aoki			
	AJ	5,422,562	06/06/95	Mammano et al.			
	AK	5,475,296	12/12/95	Vinsant et al.			
	AL	5,514,947	05/1996	Berg			
	AM	5,523,676	06/04/96	Bach et al.			
	AN	5,552,694	09/1996	Appeltans			
	AO	5,552,695	09/03/96	Schwartz			
	AP	5,568,044	10/22/96	Bittner			
	AQ	5,578,916	11/1996	Muterspaugh			
	AR	5,646,513	07/08/97	Riggio, Jr.			
	AS	5,672,958	09/30/97	Brown et al.			
	AT	5,675,240	10/1997	Fujisawa			
	AU	5,677,619	10/1997	Doluca			
	AV	5,731,694	03/24/98	Wilcox et al.			
	AW	5,751,140	05/1998	Canter			
	AX	5,793,191	08/11/98	Elmore et al.			
	AY	5,883,797	03/16/99	Amaro et al.			
	AZ	5,943,227	08/24/99	Bryson et al.			
	AAA	5,949,226	09/07/99	Tanaka et al.			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,020,729	02/01/00	Stratakos et al.			
	ACC	6,031,361	02/29/00	Burstein et al.			
	ADD	6,100,676	08/08/00	Burstein et al.			
	AEE	6,268,716	07/31/01	Burstein et al.			
	AFF	6,366,437	04/2002	Graube			
	AGG	6,369,997	04/2002	Graube			

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AHH	WO 95/31033	11/16/95	PCT				
	ALL	EP 0 547 916	06/1993	Europe				

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AJJ	Dixon, "Average Current Mode Control of Switching Power Supplies," Unitrode Switching Regulated Power Supply Design Seminar Manual, Unitrode Corporation, December 1991, pp. C1-1 to C1-14
	AKK	Dixon, "Switching Power Supply Topology Review," Unitrode Switching Regulator Power Supply Design Seminar Manual, December 1991, pp. P1-1 to P1-11
	ALL	Kassakian et al., "DC/DC Converters," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., 1991, Sec. 2.3, pp. 20-23
	AMM	Kassakian et al., "High-Frequency Switching dc/dc Converters," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., 1991, Chapter 6, pp. 103-137
	ANN	Kassakian et al., "Dynamics and Control: An Overview," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., 1991, Chapter 11, pp. 253-298
	AOO	Kassakian et al., "Discrete-Time or Sampled-Data Models," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., 1991, Sec. 12.5, pp. 313-315
	APP	Kassakian et al., "Feedback Control Design," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., 1991, Chapter 14, pp. 365-402
	AQQ	Severns et al., "Modern DC-to-DC Switchmode Power Converter Circuits," Van Nostrand Reinhold Co., December 1997, pp. 11-77

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